Malware-Analysis/Indirect Syscalls.md at main · dodosec/Malware-Analysis · GitHub

github.com/dodo-sec/Malware-Analysis/blob/main/Cobalt Strike/Indirect Syscalls.md

main

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1 contributor

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An analysis of syscall usage in Cobalt Strike Beacons

Thanks to the suggestion of my good friend <u>Nat (0xDISREL)</u>, I spent the last week digging into a Cobalt Strike beacon made with the latest leaked builder. His idea was to analyze and understand how CS approached syscalls.

Sample

This analysis was conducted in an x64 bit payload with the hash 020b20098f808301cad6025fe7e2f93fa9f3d0cc5d3d0190f27cf0cd374bcf0 4, generated by the recently leaked 4.8 version of Cobalt Strike. It's publicly available for download in <u>unpacme</u>. I will not go over unpacking the sample for the sake of brevity, but doing so is pretty straightforward and shouldn't present any problems.

A quick refresher

Before we get to the actual reversing, let's get a quick refresher on what system calls look like under Windows.



According to calling convention, arguments are setup in the appropriate registers before the instruction SYSCALL is executed, handling execution to the Kernel. One of such arguments is the code for the system call (in the picture above, it's passed via the eax register). These system calls reside in ntdll and provide evasion benefits by allowing you to avoid calling APIs that are likely hooked by AV/EDR.

How Cobalt Strike does it

During the first steps of analysis of the unpacked payload we'll come across references to qwords and calls to registers.

text:0000000180018421					loc 180018421:		: CODE XREE: sub 1800183D8+3811
text:0000000180018421	48 89	54 24	58		100_1000104111	mov	[rsn+38h+arg 18], rdy
text:0000000180018426	48 89	40 24	20			mov	[rsp+38h+var 18], rcv
text:0000000180018428	FE 15	7F 4F	61 A	A		call	cs:GetCurrentProcess
text:0000000180018431	83 30	34 08	03 0	0 01		cmp	cs:dword 18004BC6C 1
text:0000000100010431	44 99	CR 00	05 0	0.01		mov	end aby
taxt:0000000180018438	44 80	44 24	5.9			100	ng [ospi29biang 19]
taxt:0000000180018430	40 88	D0	50			Tea	rio, [isproditalg_to]
taxt:0000000180018443	40 00	54 24	20			lea	ndy [nept38biven 18]
taxt:0000000100010445	75 05	24 24	20			ina	chart los 199019459
toxt:0000000180018448	/5 OE	05 75	40.0	2 00		J112	short 100_100010450
toxt:000000010001044A	40 00	65 /F	40 0	5 00		mov	nax, cs:qworu_10004FFD0
text:0000000100010451	49 00	0.4				cal1	PCX, P10
text:0000000180018454	EP 22					dan	rax ; qworu_10004rrb0
tout:0000000100010450	CD 22					յաթ	SHOPE TOC_10001047A
toxt:0000000100010450					;		
.text:000000100010450					les 100010450.		. CODE VIEE, aut 18001830817014
.text:0000000100010450	40 00	00 70	40.0	3 00	100_100010450:		; CODE AREF; SUD_100010500+701]
.text:0000000180018458	40 00	00 79	40 0	2 00		mov	rcx, cs:qword_10004rrb8
.text:000000018001845F	40 09	00 FA	07 0	5 00		mov	cs:qword_10004BC00, rcx
.text:000000180018466	00 00	74 40	05 0	0		mov	ecx, cs:dword_10004FFE0
.text:00000010001040C	69 00	F0 0/	05 0	0		mov	cs:dw0rd_10004bC00, ecx
.text:000000180018472	48 88	10 00	~~			mov	rcx, rax
.text:000000180018475	E8 ØL	IC 00	99			call	SUD_180010086
.text:0000001800184/A							
++-0000000100010474					1		
.text:000000018001B47A					loc_18001B47A:		; CODE XREF: sub_18001B3D8+7E†j
.text:000000018001B47A .text:000000018001B47A	85 CØ				loc_18001B47A:	test	; CODE XREF: sub_18001B3D8+7E†j eax, eax
.text:00000018001B47A .text:000000018001B47A .text:000000018001B47A	85 C0 75 07				loc_18001B47A:	test jnz	; CODE XREF: sub_18001B3O8+7E†j eax, eax short_loc_18001B485
.text:00000018001B47A .text:00000018001B47A .text:000000018001B47A .text:000000018001B47C .text:000000018001B47E	85 C0 75 07 88 01	00 00	00		loc_18001B47A:	test jnz mov	; CODE XREF: sub_18001B3D8+7E†j eax, eax short loc_18001B485 eax, 1
.text:00000018001B47A .text:00000018001B47A .text:00000018001B47A .text:00000018001B47C .text:00000018001B47E .text:00000018001B483	85 C0 75 07 88 01 EB 0F	<u>00 00</u>	00		loc_18001B47A:	test jnz mov jmp	; CODE XREF: sub_18001B3D8+7E†j eax, eax short loc_18001B485 eax, 1 short loc_18001B494
.text:00000018001847A .text:000000018001847A .text:000000018001847A .text:000000018001847E .text:0000000180018483 .text:0000000180018483	85 C0 75 07 88 01 EB 0F	00 00	00		loc_18001B47A:	test jnz mov jmp	; CODE XREF: sub_18001B3D8+7E†j short loc_18001B495 eax, 1 short loc_18001B494
.text:00000013001847A .text:00000013001847A .text:00000013001847A .text:00000013001847E .text:000000130018483 .text:000000130018485 .text:000000130018485	85 C0 75 07 88 01 EB 0F	00 00	00		loc_18001B47A:	test jnz mov jmp	; CODE XREF: sub_18001B3D8+7E†j eax, eax short loc_18001B485 eax, 1 short loc_18001B494
.text:00000018001B47A .text:00000018001B47A .text:000000018001B47C .text:000000018001B47E .text:000000018001B483 .text:000000018001B485 .text:000000018001B485	85 C0 75 07 88 01 EB 0F	00 00	00		loc_18001B47A: ; loc_18001B485:	test jnz mov jmp	; CODE XREF: sub_18001B3D8+7E†j eax, eax short loc_18001B485 eax, 1 short loc_18001B494 ; CODE XREF: sub_18001B3D8+1F†j
.text:00000018001847A text:00000018001847A text:00000018001847C text:00000018001847C text:000000018001847 text:000000018001848 text:0000000180018485 text:0000000180018485	85 C0 75 07 88 01 EB 0F	00 00	00		loc_18001B47A: ; loc_18001B485:	test jnz mov jmp	; CODE XREF: sub_18001B3D8+7E†j eax, eax short loc_18001B485 eax, 1 short loc_18001B494 ; CODE XREF: sub_18001B3D8+1F†j ; sub_18001B3D8+3D†j
.text:00000018001847A .text:00000018001847A .text:00000018001847C .text:00000018001847C .text:000000180018455 .text:0000000180018485 .text:0000000180018485 .text:0000000180018485 .text:000000180018485	85 C0 75 07 88 01 EB 0F	<u>00 00</u>	00		loc_18001B47A: ; loc_18001B485:	test jnz mov jmp	; CODE XREF: sub_18001B3D8+7E†j eax, eax short loc_18001B485 eax, 1 short loc_18001B494 ; CODE XREF: sub_18001B3D8+1F†j ; sub_18001B3D8+3D†j ; sub_18001B3D8+47†j
.text:00000018001847A text:00000018001847A text:00000018001847C text:00000018001847C text:0000000180018485 text:0000000180018485 text:0000000180018485 text:0000000180018485 text:0000000180018485 text:0000000180018485	85 C0 75 07 B8 01 EB 0F	<u>00</u> 00	00		loc_18001B47A: ; loc_18001B485:	test jnz mov jmp	<pre>; CODE XREF: sub_18001B3D8+7E†j eax, eax short loc_18001B485 eax, 1 short loc_18001B494 ; CODE XREF: sub_18001B308+1F†j ; sub_18001B308+30†j ; sub_18001B308+47†j ; sub_18001B308+44†j</pre>
.text:00000018001847A text:00000018001847A text:00000018001847C text:00000018001847C text:000000018001845 text:00000018001845 text:00000018001845 text:00000018001845 text:000000018001845 text:000000018001845 text:000000018001845	85 C0 75 07 88 01 EB 0F 44 88	00 00 C3	00		loc_18001B47A: ; loc_18001B485:	test jnz mov jmp	<pre>; CODE XREF: sub_18001B3D8+7E†j eax, eax short loc_18001B485 eax, 1 short loc_18001B494 ; CODE XREF: sub_18001B3D8+1F†j ; sub_18001B3D8+47†j ; sub_18001B3D8+47†j r8d, ebx ; dwfreeType</pre>
.text:00000018001847A text:00000018001847A text:00000018001847C text:00000018001847C text:00000018001843 text:000000180018485 text:0000000180018455 text:0000000180018455 text:0000000180018455 text:0000000180018455 text:0000000180018455	85 C0 75 07 88 01 EB 0F 44 88 48 88	00 00 C3 D7	00		loc_18001B47A: ; loc_18001B485:	test jnz mov jmp mov mov	<pre>; CODE XREF: sub_18001B3D8+7E†j eax, eax short loc_18001B485 eax, 1 short loc_18001B494 ; CODE XREF: sub_18001B3D8+1F†j ; sub_18001B3D8+3D†j ; sub_18001B3D8+47†j ; sub_18001B3D8+47†j ; sub_18001B3D8+A4†j r8d, ebx ; dwFreeType rdx, rdi ; dwSize</pre>
.text:00000018001847A text:00000018001847A text:00000018001847C text:00000018001847C text:0000000180018475 text:0000000180018485 text:0000000180018485 text:0000000180018485 text:0000000180018485 text:0000000180018485 text:0000000180018485 text:0000000180018485 text:0000000180018485 text:0000000180018485	85 C0 75 07 88 01 EB 0F 44 88 48 88 48 88	00 00 C3 D7 CE	00		loc_18001B47A: ; loc_18001B485:	test jnz mov jmp mov mov mov	<pre>; CODE XREF: sub_18001B3D8+7E†j eax, eax short loc_18001B485 eax, 1 short loc_18001B494 ; CODE XREF: sub_18001B3D8+1F†j ; sub_18001B3D8+3D†j ; sub_18001B3D8+47†j ; sub_18001B3D8+47†j ; sub_18001B3D8+47†j r8d, ebx ; dwFreeType rdx, rdi ; dwSize rcx, rsi ; lpAddress</pre>
text:00000018001847A text:0000001801847A text:0000001801847C text:00000018001847 text:00000018001843 text:00000018001843 text:00000018001845 text:00000018001845 text:00000018001845 text:00000018001845 text:00000018001845 text:00000018001845 text:00000018001845	85 C0 75 07 88 01 EB 0F 44 88 48 88 48 88 FF 15	00 00 C3 D7 CE 8C 4D	00	0	loc_18001B47A: ; loc_18001B485:	test jnz mov jmp mov mov call	<pre>; CODE XREF: sub_18001B3D8+7E†j eax, eax short loc_18001B485 eax, 1 short loc_18001B494 ; CODE XREF: sub_18001B3D8+1F†j ; sub_18001B3D8+3D†j ; sub_18001B3D8+47†j ; sub_18001B3D8+44†j r8d, ebx ; dwFreeType rdx, rdi ; dwSize rcx, rsi ; lpAddress cs:VirtualFree</pre>
.text:00000018001847A text:00000018001847A text:00000018001847C text:00000018001847C text:000000180018485 text:000000180018485 text:0000000180018485 text:0000000180018485 text:0000000180018485 text:0000000180018485 text:0000000180018488 text:0000000180018488 text:0000000180018488 text:0000000180018488 text:0000000180018488	85 C0 75 07 88 01 EB 0F 44 88 48 88 48 88 FF 15	00 00 C3 D7 CE 8C 4D	00	0	loc_18001B47A: ; loc_18001B485:	test jnz mov jmp mov mov call	<pre>; CODE XREF: sub_18001B3D8+7E†j eax, eax short loc_18001B485 eax, 1 short loc_18001B494 ; CODE XREF: sub_18001B308+1F†j ; sub_18001B308+47†j ; sub_18001B308+47†j ; sub_18001B308+47†j r8d, ebx ; dwFreeType rdx, rdi ; dwSize rcx, rsi ; lpAddress cs:VirtualFree</pre>
.text:00000018001847A .text:00000018001847A .text:00000018001847C .text:00000018001847C .text:00000018001847 .text:00000018001848 .text:00000018001848 .text:00000018001848 .text:00000018001848 .text:00000018001848 .text:00000018001848 .text:00000018001848 .text:00000018001848 .text:00000018001848 .text:00000018001848 .text:00000018001848 .text:00000018001848	85 C0 75 07 88 01 EB 0F 44 88 48 88 48 88 FF 15	00 00 C3 D7 CE 8C 4D	00 01 0	0	<pre>loc_18001B47A: ; loc_18001B485: loc_18001B494:</pre>	test jnz mov jmp mov mov call	<pre>; CODE XREF: sub_18001B3D8+7E†j eax, eax short loc_18001B485 eax, 1 short loc_18001B494 ; CODE XREF: sub_18001B3D8+1F†j ; sub_18001B3D8+3D†j ; sub_18001B3D8+47†j ; sub_18001B3D8+47†j ; sub_18001B3D8+47†j r8d, ebx ; dwFreeType rdx, rdi ; dwSize rcx, rsi ; lpAddress cs:VirtualFree ; CODE XREF: sub_18001B3D8+A8†j</pre>
.text:00000018001847A text:00000018001847A text:00000018001847C text:00000018001847 text:000000180018485 text:000000180018485 text:000000180018485 text:0000000180018485 text:000000018001845 text:000000018001845 text:0000000180018485 text:0000000180018485 text:0000000180018485 text:000000018001848 text:000000018001848 text:000000018001848 text:000000018001848 text:000000018001848 text:000000018001848	85 C0 75 07 88 01 EB 0F 44 88 48 88 48 88 FF 15 48 88	00 00 C3 D7 CE 8C 4D 5C 24	00 01 0 40	0	<pre>loc_18001B47A: ; loc_18001B485: loc_18001B485:</pre>	test jnz mov jmp mov mov call mov	<pre>; CODE XREF: sub_18001B3D8+7E†j eax, eax short loc_18001B485 eax, 1 short loc_18001B494 ; CODE XREF: sub_18001B3D8+1F†j ; sub_18001B3D8+47†j ; sub_18001B3D8+47†j ; sub_18001B3D8+A4†j r8d, ebx ; dwFreeType rdx, rdi ; dwSize rcx, rsi ; lpAddress cs:VirtualFree ; CODE XREF: sub_18001B3D8+A8†j rbx, [rsp+38h+arg_0]</pre>
.text:00000018001847A text:00000018001847A text:00000018001847C text:00000018001847C text:0000000180018475 text:0000000180018485 text:0000000180018485 text:0000000180018485 text:0000000180018485 text:0000000180018485 text:0000000180018485 text:0000000180018488 text:0000000180018488 text:0000000180018488 text:0000000180018488 text:0000000180018494 text:0000000180018494 text:0000000180018494	85 C0 75 07 88 01 EB 0F 44 88 48 88 48 88 FF 15 48 88 48 88	00 00 C3 D7 CE 8C 4D 5C 24 74 24	00 01 0 40 48	0	<pre>loc_18001B47A: ; loc_18001B485: loc_18001B494:</pre>	test jnz mov jmp mov call mov mov	<pre>; CODE XREF: sub_18001B3D8+7Etj eax, eax short loc_18001B485 eax, 1 short loc_18001B494 ; CODE XREF: sub_18001B3D8+1Ftj ; sub_18001B3D8+3Dtj ; sub_18001B3D8+47tj ; sub_18001B3D8+47tj ; sub_18001B3D8+47tj r8d, ebx ; dwFreeType rdx, rdi ; dwSize rcx, rsi ; lpAddress cs:VirtualFree ; CODE XREF: sub_18001B3D8+A8tj rbx, [rsp+38h+arg_0] rsi, [rsp+38h+arg_0]</pre>
.text:00000018001847A .text:000000018001847A .text:000000018001847C .text:0000000180018475 .text:0000000180018485 .text:0000000180018485 .text:0000000180018455 .text:0000000180018455 .text:0000000180018455 .text:0000000180018455 .text:0000000180018455 .text:0000000180018455 .text:0000000180018455 .text:0000000180018455 .text:0000000180018455 .text:0000000180018455 .text:0000000180018455 .text:0000000180018455 .text:0000000180018455 .text:0000000180018455 .text:0000000180018455 .text:0000000180018455 .text:000000180018455 .text:000000180018455 .text:000000180018455 .text:000000180018455 .text:000000180018455 .text:000000180018455	85 C0 75 07 88 01 EB 0F 44 88 48 88 46 88 FF 15 48 88 48 88 48 88 48 88	00 00 C3 D7 CE 8C 4D 5C 24 74 24 C4 30	00 01 0 40 48	0	<pre>loc_18001B47A: ; loc_18001B485: loc_18001B485:</pre>	test jnz mov jmp mov mov call mov mov add	<pre>; CODE XREF: sub_18001B3D8+7Etj eax, eax short loc_18001B485 eax, 1 short loc_18001B494 ; CODE XREF: sub_18001B3D8+1Ftj ; sub_18001B3D8+47tj ; sub_18001B3D8+47tj ; sub_18001B3D8+47tj r8d, ebx ; dwFreeType rdx, rdi ; dwSize rcx, rsi ; lpAddress cs:VirtualFree ; CODE XREF: sub_18001B3D8+A8tj rbx, [rsp+38h+arg_0] rsi, [rsp+38h+arg_8] rsp, 38h</pre>
.text:00000018001847A text:00000018001847A text:00000018001847C text:00000018001847C text:000000180018485 text:000000180018485 text:0000000180018485 text:0000000180018485 text:0000000180018485 text:0000000180018485 text:0000000180018485 text:0000000180018488 text:0000000180018488 text:0000000180018488 text:0000000180018488 text:0000000180018484 text:0000000180018499 text:0000000180018499 text:0000000180018499 text:0000000180018499	85 C0 75 07 88 01 EB 0F 44 88 48 88 48 88 48 88 FF 15 48 88 48 88 48 88 48 88 48 88 48 88 48 88 48 88 5F	00 00 C3 D7 CE 8C 4D 5C 24 74 24 C4 30	00 01 0 40 48	0	loc_18001B47A: ; loc_18001B485: loc_18001B494:	test jnz mov jmp mov mov call mov mov add pop	<pre>; CODE XREF: sub_18001B3D8+7Etj eax, eax short loc_18001B485 eax, 1 short loc_18001B494 ; CODE XREF: sub_18001B308+1Ftj ; sub_18001B308+3Dtj ; sub_18001B308+47tj ; sub_18001B308+47tj r8d, ebx ; dwFreeType rdx, rdi ; dwSize rcx, rsi ; lpAddress cs:VirtualFree ; CODE XREF: sub_18001B308+ABtj rbx, [rsp+38h+arg_0] rsi, [rsp+38h+arg_0] rsi, [rsp+38h+arg_0] rsi, 30h</pre>
Lext:00000018001847A Lext:000000018001877 Lext:0000000180018472 Lext:0000000180018472 Lext:0000000180018403 Lext:0000000180018403 Lext:0000000180018403 Lext:0000000180018405 Lext:0000000180018455 Lext:0000000180018455 Lext:0000000180018458 Lext:0000000180018458 Lext:0000000180018458 Lext:0000000180018458 Lext:0000000180018458 Lext:0000000180018458 Lext:0000000180018459 Lext:0000000180018499 Lext:0000000180018499 Lext:0000000180018499 Lext:0000000180018499 Lext:0000000180018495	85 C0 75 07 88 01 EB 0F 44 88 48 88 48 88 48 88 48 88 48 88 48 88 48 88 5F C3	00 00 C3 D7 CE 8C 4D 5C 24 74 24 C4 30	00 01 0 40 48	0	<pre>loc_18001B47A: ; loc_18001B485: loc_18001B494:</pre>	test jnz mov jmp mov mov call mov mov add pop retn	<pre>; CODE XREF: sub_18001B3D8+7Etj eax, eax short loc_18001B485 eax, 1 short loc_18001B494 ; CODE XREF: sub_18001B3D8+1Ftj ; sub_18001B3D8+3Dtj ; sub_18001B3D8+47tj ; sub_18001B3D8+47tj r8d, ebx ; dwfreeType rdx, rdi ; dwSize rcx, rsi ; lpAddress cs:VirtualFree ; CODE XREF: sub_18001B3D8+ABtj rbx, [rsp+38h+arg_0] rsi, [rsp+38h+arg_0] rsi,</pre>

Inspecting said qwords will lead us to the .data section, where they don't hold any values (yet).

.data:000000018004FFC0											: sub 18001B4A4+79†r
.data:000000018004FFC8	??	??	??	??					dword 18004FFC8	dd ?	: DATA XREF: sub 18001B4A4+B61r
.data:000000018004FFCC	??	??	??	??					-	align 10h	
.data:000000018004FFD0	??	??	22	??	??	??	??	??	aword 18004FFD0	da ?	: DATA XREF: sub 18001B3D8+301r
.data:000000018004FFD0										1.00	; sub 18001B3D8+72†r
.data:000000018004FFD0											; mw indirect syscalls setup+9Eto
.data:000000018004FFD8	??	??	??	??	??	??	??	??	gword 18004FFD8	da ?	; DATA XREF: sub 18001B3D8+3Ftr
.data:000000018004FFD8										1	: sub 18001B3D8:loc 18001B458†r
.data:000000018004FFE0	??	??	??	??					dword 18004FFE0	dd ?	: DATA XREF: sub 18001B3D8+8E1r
.data:000000018004FFE4	??	??	??	??					-	align 8	
.data:000000018004FFE8	??	??	??	??	??	??	??	??	gword 18004FFE8	dq ?	; DATA XREF: sub 18001AD38+24↑r
.data:000000018004FFE8										1.00	; mw indirect syscalls setup+BF↑o
.data:000000018004FFF0	??	??	??	??	??	??	??	??	aword 18004FFF0	da ?	: DATA XREF: sub 18001AD38+3A1r
.data:000000018004FFF8	??	??	??	??					dword 18004FFF8	dd ?	; DATA XREF: sub 18001AD38+4D1r
.data:000000018004FFFC	??	??	??	??					-	align 20h	
.data:000000180050000	??	??	??	??	??	??	??	??	gword 180050000	da ?	: DATA XREF: sub 18001B19C+241r
.data:0000000180050000											: mw indirect syscalls setup+E0to
.data:0000000180050008	??	??	??	??	??	??	??	??	gword 180050008	da ?	; DATA XREF: sub 18001B19C+3Afr
.data:0000000180050010	??	??	??	??					dword 180050010	dd ?	: DATA XREF: sub 18001819C+4D†r
.data:0000000180050014	??	??	??	??					-	align 8	
.data:0000000180050018	??	??	22	??	??	??	??	??	aword 180050018	da ?	: DATA XREF: sub 18001B11C+1D1r
.data:0000000180050018											: mw indirect syscalls setup+101to
.data:0000000180050020	??	??	??	??	??	??	??	??	aword 180050020	da ?	: DATA XREF: sub 18001811C+381r
.data:0000000180050028	??	??	22	??					dword 180050028	dd ?	: DATA XREF: sub 18001B11C+4B1r
.data:000000018005002C	- 22		- 22							align 10h	,
.data:0000000180050030	- 22		- 22		??	??	??	??	aword 180050030	da ?	: DATA XREF: sub 18001ABB8+471r
.data:0000000180050030											; sub 18001ABB8+81†r
.data:0000000180050030											: mw indirect syscalls setup+1221o
.data:0000000180050038	22	>>	??	22	22	22	22	22	aword 180050038	da ?	: DATA XREF: sub 18001ABB8+401r
.data:0000000180050038										and a	: sub 18001ABB8+7Atr
data:0000000180050040	>>	>>	>>	,,,					dword 180050040	dd ?	: DATA XREE: sub 18001A888:loc 18001AC87tr
.data:0000000180050044	- 22		- 22							align 8	,
.data:0000000180050048	- 22	>>	- 22		22	??	??	22	aword 180050048	da ?	: DATA XREF: sub 18001AF28+3D1r
data:000000180050048									41010_20000010	and .	: mw indirect syscalls setup+143to
data:0000000180050050		>>	>>	,,,		,,,	22	22	aword 180050050	da ?	: DATA XREF: sub 18001AF28+361r
.data:0000000180050058	- 22		- 22						dword 180050058	dd ?	: DATA XREF: sub 18001AF28:loc 18001AFCEtr
data:000000180050050	- 55		- 55	55					41101 4_100050050	align 20h) bain and 1 505_100014 201200_100014 Cert
data:0000000180050050	- 55	- 55	- 55	55	>>	>>	>>	>>	aword 180050060	da ?	: DATA XREE: sub 180018030+3Etc
data:0000000180050060									410.0_20000000	and .	: mw indirect syscalls setup+1641o
data:0000000180050058	>>	>>	>>	>>	>>	>>	>>	>>	aword 180050068	da 2	DATA XREE: sub 180018030+37tr
data:000000180050070	- 55		- 55	55	. T.				dword 180050070	dd ?	: DATA XREE: sub 180018030:10c 1800180D2tc
data:000000180050074	- 55	- 55	- 55	55						align 8	, bank aner 300_0000000000000000000000000000000000
data:0000000130050074	- 55	55	- 55	55	>>		>>	>>	aword 180050078	da ?	• DATA XREE: sub 180010030+161c
data:0000000180050078									duo. a_100030010	uq .	; my indirect syscalls setup+185to
data:0000000180050078	>>	>>	>>	>>	>>	>>	>>	>>	aword 180050020	da 2	: DATA XREE: sub 18001AA30+34**
data:0000000180050080	55	55	55	55	11				dword 180050080	dd 2	: DATA XREE: sub 180010030+47*r
data 0000000100050080	55	- 55	55	55						align 10h	, which and it ave_avevaceset()
										01180 100	

Inspecting other references to these addresses will land us in a function that looks a lot like an import by hash routine - there are repeated calls to the same function, each time passing a different hexadecimal value and a .data section address among its arguments.

.text:000000018001B6B	0									
.text:000000018001B6B	0 48	89	5C	24	10				mov	[rsp-8+arg_8], rbx
.text:000000018001B6B	5 48	89	7C	24	18				mov	[rsp-8+arg 10], rdi
.text:000000018001B6B	A 55	j.							push	rbp
.text:000000018001B6B	B 48	8D	AC	24	90	E1	FF	FF	lea	rbp, [rsp-1E70h]
.text:000000018001B60	3 B8	70	1F	00	00				mov	eax, 1F70h
.text:000000018001B60	8 E8	33	33	00	00				call	alloca_probe
.text:000000018001B60	D 48	2B	EØ						sub	rsp, rax
.text:000000018001B6D	0 83	64	24	30	00				and	[rsp+1F70h+var_1F40], 0
.text:000000018001B6D	5 44	8D	40	СС					lea	r8d, [rax-34h] ; Size
.text:000000018001B6D	9 48	8D	4C	24	34				lea	<pre>rcx, [rsp+1F70h+var_1F3C] ; void *</pre>
.text:000000018001B6D	E 33	D2							xor	edx, edx ; Val
.text:000000018001B6E	0 E8	8B	1D	00	00				call	memset
.text:000000018001B6E	5 48	83	A5	80	1E	00	00	00	and	[rbp+1E70h+arg_0], 0
.text:000000018001B6E	D BF	F4	01	00	00				mov	edi, 1F4h
.text:000000018001B6F	2 40	8D	85	80	1E	00	00		lea	r8, [rbp+1E70h+arg_0]
.text:000000018001B6F	9 48	8D	4C	24	30				lea	<pre>rcx, [rsp+1F70h+var_1F40] ; void *</pre>
.text:000000018001B6F	E 88	D7							mov	edx, edi
.text:000000018001B70	0 E8	EF	FØ	FF	FF				call	sub_18001A7F4
.text:000000018001B70	5 48	8B	9D	80	1E	60	00		mov	rbx, [rbp+1E70h+arg_0]
.text:000000018001B70	C 48	8D	05	8D	48	03	00		lea	rax, qword_18004FFA0
.text:000000018001B71	3 48	8D	4C	24	30				lea	rcx, [rsp+1F70h+var_1F40]
.text:000000018001B71	8 40	8B	C3						mov	r8, rbx
.text:000000018001B71	B 41	B9	69	7A	2B	B1			mov	r9d, 0B12B7A69h
.text:000000018001B72	1 88	D7							mov	edx, edi
.text:000000018001B72	3 48	89	44	24	20				mov	[rsp+1F70h+var_1F50], rax
.text:000000018001B72	8 E8	ØF	F0	FF	FF				call	sub_18001A73C
.text:000000018001B72	D 48	8D	05	84	48	03	00		lea	rax, qword_18004FFB8
.text:000000018001B73	4 48	8D	4C	24	30				lea	rcx, [rsp+1F70h+var_1F40]
.text:000000018001B73	9 41	. B9	8B	CF	08	C5			mov	r9d, 0C508CF8Bh
.text:000000018001B73	F 40	8B	C3						mov	r8, rbx
.text:000000018001B74	2 88	D7							mov	edx, edi
.text:000000018001B74	4 48	89	44	24	20				mov	[rsp+1F70h+var_1F50], rax
.text:000000018001B74	9 E8	EE	EF	FF	FF				call	sub_18001A73C
.text:000000018001B74	E 48	8D	05	7B	48	03	00		lea	rax, qword_18004FFD0
.text:000000018001B75	5 48	8D	4C	24	30				lea	rcx, [rsp+1F70h+var_1F40]
.text:000000018001B75	A 41	B9	23	21	AF	35			mov	r9d, 35AF2123h
.text:000000018001B76	0 4 0	8B	C3						mov	r8, rbx
.text:000000018001B76	3 88	D7							mov	edx, edi
.text:00000018001B76	5 48	89	44	24	20				mov	[rsp+1F70h+var_1F50], rax
.text:000000018001B76	A E8	CD	EF	FF	FF				call	sub_18001A73C
.text:000000018001B76	F 48	8D	05	72	48	03	00		lea	rax, gword_18004FFE8

Case closed then, the empty qwords would receive pointers to the resolved API functions, right? All that's left is to identify the hashing algorithm and start renaming things? Well, not quite. This write-up is not called "analyzing import by hash", after all.

Let's take a look at the function that's called before all the hashes start showing up. I've named it mw_prepare_indirect_syscalls.

tout.0000000100014014 45 00 50			-12
.text:000000010001A01A 4C 6D E9		mov	
.text:000000018001A810 41 88 20 20 20 20		mov	
.text:0000000180018823 4C 88 48 60		mov	r9, [rax+1EB64.ProcessEnvironmentBlock]
.text:0000000180018827 40 88 51 18		mov	rio, [r9+P2864.Lar]
.text:000000018001A828 49 83 C2 10		add	r10, 10n
.text:000000018001A82F 4D 88 0A		mov	r9, [r10+(PEB_LDR_DATA.InLoadOrderModuleList.Flink-10h)]
.text:00000018001A832			
.text:000000018001A832	loc_18001A832:		; CODE XREF: mw_prepare_indirect_syscalls+584j
.text:000000018001A832			; mw_prepare_indirect_syscalls+654j
.text:00000018001A832			; mw_prepare_indirect_syscalls+75+j
.text:00000018001A832			; mw_prepare_indirect_syscalls+83+j
.text:00000018001A832			; mw_prepare_indirect_syscalls+92↓j
.text:000000018001A832 4D 3B CA		cmp	r9, r10
.text:00000018001A835 0F 84 A9 01 00 00		jz	loc_18001A9E4
.text:000000018001A838 49 88 79 30		mov	rdi, [r9+LDR_DATA_TABLE_ENTRY.DllBase]
.text:00000018001A83F 4D 88 09		mov	r9, [r9]
.text:00000018001A842 48 63 47 3C		movsxd	rax, [rdi+IMAGE_DOS_HEADER.e_lfanew]
.text:000000018001A846 88 8C 38 88 00 00 00		mov	ecx, [rax+rdi+IMAGE_NT_HEADERS64.OptionalHeader.DataDirectory.VirtualAddress]
.text:000000018001A84D 85 C9		test	ecx, ecx
.text:000000018001A84F 74 E1		jz	short loc_18001A832
.text:000000018001A851 48 8D 34 0F		lea	rsi, [rdi+rcx]
.text:000000018001A855 83 7E 18 00		cmp	<pre>[rsi+IMAGE_EXPORT_DIRECTORY.NumberOfNames], 0</pre>
.text:000000018001A859 74 D7		jz	short loc_18001A832
.text:00000018001A858 88 4E 0C		mov	ecx, [rsi+IMAGE_EXPORT_DIRECTORY.Name]
.text:00000018001A85E 88 04 39		mov	eax, [rcx+rdi] ; first 4 bytes of dll name
.text:00000018001A861 41 08 C0		or	eax, r8d
.text:000000018001A864 3D 6E 74 64 6C		cmp	eax, 'ldtn'
.text:00000018001A869 75 C7		jnz	short loc_18001A832
.text:00000018001A868 88 44 39 04		mov	eax, [rcx+rdi+4]
.text:00000018001A86F 41 08 C0		or	eax, r8d
.text:00000018001A872 3D 6C 2E 64 6C		cmp	eax, 'ld.l'
.text:00000018001A877 75 89		jnz	short loc_18001A832
.text:00000018001A879 0F B7 44 39 08		movzx	eax, word ptr [rcx+rdi+8]
.text:000000018001A87E 66 83 C8 20		or	ax, 20h
.text:000000018001A882 66 83 F8 6C		cmp	ax, '1'
.text:00000018001A886 75 AA		jnz	short loc_18001A832
.text:00000018001A888 88 5E 1C		mov	ebx, [rsi+IMAGE_EXPORT_DIRECTORY.AddressOfFunctions]
.text:00000018001A888 88 6E 20		mov	ebp, [rsi+IMAGE_EXPORT_DIRECTORY.AddressOfNames]
.text:000000018001A88E 8B 46 24		mov	eax, [rsi+IMAGE_EXPORT_DIRECTORY.AddressOfNameOrdinals]
.text:000000018001A891 44 88 C2		mov	r8d, edx
.text:00000018001A894 48 03 C7		add	rax, rdi
.text:00000018001A897 48 03 DF		add	rbx, rdi
.text:000000018001A89A 48 03 EF		add	rbp, rdi
.text:000000018001A89D 49 88 CD		mov	rcx, r13 ; void *
.text:000000018001A8A0 49 C1 E0 04		shl	r8, 4 ; Size
.text:000000018001A8A4 33 D2		xor	edx, edx ; Val
.text:000000018001A8A6 48 89 5C 24 20		mov	[rsp+68h+AddressOfFunctions], rbx
.text:000000018001A8AB 48 89 84 24 88 00 00 00		mov	[rsp+68h+arg_18], rax
.text:000000018001A8B3 48 89 6C 24 28		mov	[rsp+68h+var_40], rbp
.text:00000018001A8B8 45 33 FF		xor	r15d, r15d
.text:000000018001A8BB E8 B0 2B 00 00		call	memset

Preparing system calls

The first part of it is run of the mill PEB walking and PE parsing to get names of exported functions. Note also that there is a check of IMAGE_EXPORT_DIRECTORY.Name against ntdll.dll very slightly obfuscated (it's just written backwards and split over three cmp instructions). This tells us the author is only interested in ntdll. That makes sense, considering they're after syscalls. There is a memset , to which we'll come back later.

The next block of code will check the function name for the prefixes Ki and Zw.If either prefix matches there is a call to the hashing function, which is a ROR 8 ADD algorithm that iterates over each word and uses 0x52964EE9 as a hardcoded XOR key.



A function starting with Ki will only be used if its hash matches 0x8DCD4499; on a 22H2 version of Windows 10 I couldn't find an export from ntdll that matched such value. This routine then will act on at most one function starting with Ki and all starting with Zw. Appropriate values will populate a structure whose address was supplied to mw_prepare_indirect_syscalls - I've decided to call it syscalls_organized_by_hash. It is described below.

```
struct syscalls_organized_by_hash {
  DWORD function_hash;
  DWORD ntdll_address_of_function;
  QWORD ptr_to_function_syscall_block;
  };
```

function_hash is the calculated hash for the exported function; ntdll
address of function is an address to the function's code as pointed to
by IMAGE_EXPORT_DIRECTORY.AddressOfFunctions;

ptr_to_function_syscall_block is a pointer to the system call gadget related to said function, which resides in ntdll.dll memory. Remember the memset call earlier? It's used to zero that structure out. The r13 register

points to it, and the additions at each address confirm the size of each struct member. After all the Zw prefixed functions are placed in the structure, an algorithm will sort their positions according to the ntdll_address_of_function, from lowest to highest. After this is done, the struct will contain the hashes, addresses of functions in the ntdll executable and pointers to the syscall gadgets for all functions with a Zw prefix, sorted in ascending order according to the ntdll_address_of_function values.

Setting up the syscalls structure

Going back to the function that resembled import by hash with what we've learned, we can see the that mw_get_indirect_syscalls_by_hash is supplied the syscalls_organized_by_hash, alongside the hash and a pointer to those empty qwords. After using the hashing algorithm to generate enums from ntdll exports, we can solve the hashes to see which APIs they intended to get the syscall code blocks to.

.text:0000000180018700	E8	EF	FØ	EF.	FF		
.text:000000018001B705	48	88	9D	80	1E	00	00
.text:000000018001B70C	48	8D	05	8D	48	03	66
.text:000000018001B713	48	8D	4C	24	30		
.text:000000018001B718	4C	8B	C 3				
.text:000000018001B71B	41	B 9	69	7A	2B	B1	
.text:0000000180018721	88	D7					
.text:000000018001B723	48	89	44	24	20		
.text:000000018001B728	E8	ØF	FØ	FF	FF		
.text:000000018001B72D	48	8D	05	84	48	03	00
.text:00000018001B734	48	8D	4C	24	30		
.text:000000018001B739	41	B 9	8B	CF	68	C5	
.text:000000018001B73F	4 C	88	С3				
.text:000000018001B742	8B	D7					
.text:000000180018744	48	89	44	24	20		
.text:0000000180018749	E8	EE	EF	FF	FF		
.text:000000018001B74E	48	8D	05	7B	48	03	00
.text:000000018001B755	48	8D	4C	24	30		
.text:000000018001B75A	41	B 9	23	21	AF	35	
.text:000000018001B760	4C	88	C 3				
.text:000000018001B763	8B	D7					
.text:000000018001B765	48	89	44	24	20		
.text:000000018001B76A	E8	CD	EF	FF	FF		
.text:000000018001876F	48	8D	05	72	48	03	00
.text:000000018001B776	48	8D	4C	24	30		
.text:000000018001B77B	41	89	ØB	89	9B	85	
.text:000000018001B781	4C	8B	C 3				
.text:000000018001B784	8B	D7					
.text:000000018001B786	48	89	44	24	20		
.text:000000018001B78B	E8	AC	EF	FF	FF		
.text:000000018001B790	48	8D	05	69	48	03	00
.text:0000000180018797	48	8D	4C	24	30		
.text:000000018001879C	41	B 9	75	FØ	5E	76	
.text:000000018001B7A2	4C	88	С3				
.text:000000018001B7A5	8B	D7					
.text:000000018001B7A7	48	89	44	24	20		
.text:000000018001B7AC	E8	88	EF	FF	FF		
.text:000000018001B7B1	48	8D	05	60	48	03	66
.text:000000018001B7B8	48	8D	4C	24	30		
.text:000000018001B7BD	41	B9	65	E1	4E	66	
.text:00000018001B7C3	4C	88	C3				
.text:000000018001B7C6	8B	D7					
.text:000000018001B7C8	48	89	44	24	20		
.text:000000018001B7CD	E8	6A	EF	FF	FF		

call _indirect_syscalls mw_prepare_indirect_syscalls
rbx, [rbp+lf70h+arg.[0]
rax, syscalls_struct
rcx, [rsp+lF70h+syscalls_organized_by_hash]
r8, rbx
r9d, NT_ZwAllocateVirtualMemory
edx, edi
[rsp+lF70h+ptr_to_syscalls_struct], rax
sector inject_syscalls_helparth mov lea lea call mw_get_indirect_syscall_by_hash
rax, syscalls_struct.zwProtectVirtualMemory_ptr_to_syscall_block
rcx, [rspt+F70H-syscalls_organized_by_hash]
r9d, NT_ZwProtectVirtualMemory lea lea mov mov r8, rbx edx, edi
[rsptiF70h+ptr_to_syscalls_struct], rax
mm_get_indirect_syscall_by_hash
rax, syscalls_struct.2wFreeVirtualMemory_ptr_to_syscall_block
rcx, [rspt+F70h+syscalls_organized_by_hash]
r94, NT_2wFreeVirtualMemory
e8, rbv edx, edi mov call mov mov r8, rbx mov edx. edi edx, edi [rspif70h+ptr_to_syscalls_struct], rax mw_get_indirect_syscall_by_hash rax, syscalls_struct.ZwGetContextThread_ptr_to_syscall_block rcx, [rspif70h+syscalls_organized_by_hash] r9d, NT_ZwGetContextThread call lea lea mov mov r8, rbx r8, rbx edx, edi [rspif70h+ptr_to_syscalls_struct], rax mw_get_indirect_syscall_by_hash rax, syscalls_struct.ZwSetContextThread_ptr_to_syscall_block rcx, [rspif780h+syscalls_organized_by_hash] r9d, NT_ZwSetContextThread mov call lea lea rso, m__wsetContextInread
r8, rbx
edx, edi
[rsp+1F70h+ptr_to_syscalls_struct], rax call lea mov r8, rbx edx, edi mov mov [rsp+1F70h+ptr_to_syscalls_struct], rax
call mw_get_indirect_syscall_by_hash

mw_get_indirect_syscalls_by_hash works by looking for the supplied hash in the syscalls_organized_by_hash structure. Once that is found, it will retrieve the pointer to the syscall code block and call a function that validates said block - mw_validate_syscall_codeblock.

The way the verification works is simple. It will loop through the syscalls_organized_by_hash struct (they are actually organized by ascending order of ntdll_address_of_function, but I didn't know that back when I created the structure) until it finds the supplied hash. The

functions are organized inside ntdll by ascending order of syscall codes - a function that uses code 0x1 is succeeded by one that uses code 0x2 and so forth. Because of this, once a hash is found the counter in edi will be equal to the syscall code. The validation function checks for the op codes of the SYSCALL and RET instructions.



Once the desired entry is found, a new structure (which I've named syscalls) will receive a pointer to the syscall code block, a pointer to the SYSCALL instruction and the value of the syscall code. Although the code is a dword, I've made all members of struct qwords for convenience (that way I don't need to create a member for padding between different syscalls entries). The struct is as follows:

```
struct syscalls {
QWORD ptr_to_syscall_block;
QWORD ptr_to_syscall_instruction;
QWORD syscall_code;
};
```

Now all that's left is use that model to generate the structure that will result from setting up the syscalls and apply it to the range of qwords that are passed to the mw_get_indirect_syscalls_by_hash function. Following cross-references to each member will lead us to places where the structure is used in the beacon code.

```
00000000
00000000 syscalls struc ; (sizeof=0x150, align=0x8, copyof_114)
00000000
                                                  ; XREF: .data:syscalls_struct/r
00000000 ZwAllocateVirtualMemory_ptr_to_syscall_block dq ?
00000000
                                                  ; XREF: sub_18001B2B4+43/r
0000000
                                                  ; sub 18001B2B4+7F/r
00000008 ZwAllocateVirtualMemory_ptr_to_syscall_instruction dq ?
0000008
                                                  ; XREF: sub 18001B2B4+3C/r
                                                    sub_18001B2B4+78/r
00000008
00000010 ZwAllocateVirtualMemory_syscall_code dq ?
00000010
                                                  ; XREF: sub 18001B2B4:loc 18001B365/r
00000018 ZwProtectVirtualMemory_ptr_to_syscall_block dq ?
                                                  ; XREF: sub_18001B4A4+44/r
00000018
                                                  ; sub 18001B4A4+80/r ...
00000018
00000020 ZwProtectVirtualMemory_ptr_to_syscall_instruction dq ?
                                                  ; XREF: sub 18001B4A4+3D/r
00000020
00000020
                                                  ; sub 18001B4A4+79/r
00000028 ZwProtectVirtualMemory_syscall_code dq ?
00000028
                                                  ; XREF: sub_18001B4A4+B6/r
00000030 ZwFreeVirtualMemory_ptr_to_syscall_block dq ?
                                                  ; XREF: sub 18001B3D8+30/r
00000030
00000030
                                                  ; sub_18001B3D8+72/r ...
00000038 ZwFreeVirtualMemory_ptr_to_syscall_instruction dq ?
00000038
                                                  ; XREF: sub 18001B3D8+3F/r
                                                   sub 18001B3D8:loc 18001B458/r
00000038
00000040 ZwFreeVirtualMemory_syscall_code dq ?
                                                  ; XREF: sub 18001B3D8+8E/r
00000048 ZwGetContextThread_ptr_to_syscall_block dq ?
00000048
                                                  ; XREF: mw ZwGetContextThread wrap+24/r
                                                  ; mw_indirect_syscalls_setup+BF/o
00000048
00000050 ZwGetContextThread ptr to syscall instruction dq ?
                                                  ; XREF: mw_ZwGetContextThread_wrap+3A/r
00000050
00000058 ZwGetContextThread syscall code dq ?
                                                  ; XREF: mw_ZwGetContextThread_wrap+4D/r
00000060 ZwSetContextThread_ptr_to_syscall_block dq ?
                                                  ; XREF: sub 18001B19C+24/r
00000060
00000060
                                                  ; mw_indirect_syscalls_setup+E0/o
00000068 ZwSetContextThread_ptr_to_syscall_instruction dq ?
00000068
                                                  ; XREF: sub 18001B19C+3A/r
                                                   XREF: sub 18001B19C+4D/r
00000070 ZwSetContextThread syscall code dq ?
00000078 ZwResumeThread_ptr_to_syscall_block dq ?
00000078
                                                  ; XREF: sub_18001B11C+1D/r
00000078
                                                  ; mw indirect syscalls setup+101/o
00000080 ZwResumeThread_ptr_to_syscall_instruction dq ?
00000080
                                                  ; XREF: sub 18001B11C+38/r
00000088 ZwResumeThread_syscall_code dq ?
                                                  ; XREF: sub 18001B11C+4B/r
00000090 ZwCreateThreadEx ptr to syscall block dq ?
00000090
                                                  ; XREF: sub 18001ABB8+47/r
00000090
                                                  ; sub 18001ABB8+81/r ...
00000098 ZwCreateThreadEx_ptr_to_syscall_instruction dq ?
                                                  ; XREF: sub_18001ABB8+40/r
00000098
                                                  ; sub_18001ABB8+7A/r
00000098
                                                  ; XREF: sub_18001ABB8:loc_18001AC87/r
000000A0 ZwCreateThreadEx_syscall_code dq ?
```

Syscall usage

Let's take a wrapper function used to get thread context as an example.

.text:000000018001AD38 48 89 5C 24 08		mov	[rsp+arg_0], rbx
.text:00000018001AD3D 57		push	rdi
.text:00000018001AD3E 48 83 EC 20		sub	rsp, 20h
.text:000000018001AD42 83 3D 27 0F 03 00 00		cmp	cs:dword_180048C70, 0
.text:000000018001AD49 48 88 DA		mov	rbx, rdx
.text:000000018001AD4C 48 88 P9		mov	rd1, rcx
.text:00000018001AD4F 74 53		jz	short loc_18001ADA4
.text:000000018001AD51 88 05 15 0F 03 00		mov	eax, cs:use_syscalls_flag
.text:00000018001AD57 83 F8 01		cmp	eax, 1
.text:000000018001AD5A 75 11		jnz	short loc_18001AD6D
.text:000000018001AD5C 4C 88 05 85 52 03 00		mov	<pre>r8, cs:syscalls_struct.ZwGetContextThread_ptr_to_syscall_block</pre>
.text:00000018001AD63 4D 85 C0		test	r8, r8
.text:00000018001AD66 74 05		jz	short loc_18001AD6D
.text:000000018001AD68 41 FF D0		call	r8
.text:000000018001AD68 E8 2C		jmp	short loc_18001AD99
.text:00000018001AD6D	;		
.text:000000018001AD6D			
.text:00000018001AD5D	loc_18001AD6D:		; CODE XREF: sub_18001AD38+22†j
.text:00000018001AD6D			; sub_18001AD38+2E†j
.text:000000018001AD6D 83 F8 02		cmp	eax, 2
.text:00000018001AD70 75 32		jnz	short loc_18001ADA4
.text:000000018001AD72 48 88 0D 77 52 03 00		mov	<pre>rcx, cs:syscalls_struct.ZwGetContextThread_ptr_to_syscall_instruction</pre>
.text:00000018001AD79 48 85 C9		test	rcx, rcx
.text:00000018001AD7C 74 26		jz	short loc_18001ADA4
.text:000000018001AD7E 48 89 0D DB 0E 03 00		nov	cs:ptr_to_syscall_instruction, rcx
.text:000000018001AD85 88 0D 6D 52 03 00		mov	ecx, dword ptr cs:syscalls struct.ZwGetContextThread syscall code
.text:000000018001AD88 89 0D D7 0E 03 00		mov	cs:syscall code, ecx
.text:000000018001AD91 48 88 CF		mov	rcx, rdi
.text:00000018001AD94 E8 00 23 00 00		call	mv direct syscall ZwGetContextThread
.text:000000018001AD99			
.text:00000018001AD99	loc 18001AD99:		: CODE XREF: sub 18001AD38+3311
.text:00000018001AD99 85 C0	-	test	cax, cax
.text:000000018001AD98 75 07		inz	short loc 18001ADA4
.text:000000018001AD9D 88 01 00 00 00		mov	eax. 1
text:000000018001ADA2 EB 0C		dmb	short loc 180014080
text:00000018001ADA4	:		
text:00000018001ADA4	·		
text:00000018001ADA4	loc 18001ADA4:		: CODE XREE: sub 18001AD38+17ti
.text:000000018001ADA4			: sub 18001AD38+38†i
text:000000180014D44			sub 180014D38+4411
text:000000018001404			sub 180014D38+63ti
text:000000180014044 48 88 03		mov	rdy, rby : lnContext
text:000000180010007 48 88 CF		mov	nov. ndi i hThread
tevt:000000180014044 FF 15 48 54 01 00		call	cs:GetThreadContext
text:0000000100010001000		COLL	
+avt+0000000100010001	loc 189914089;		· CODE XDEE: sub 180014D38+64*5
text: 000000180014080 48 88 50 24 30	100_1000100001	mov	chy. [cont28htang A]
tevt:0000000180014085 48 83 C4 20		add	rsp. 28h
taut:00000000000000000000000000000000000		000	adi .
tevt:000000120012002		ceta	101
+av++0000000100010001	cub 100014020	andn	
. (EXT:000000100010001000	200 T000TWD20	endp	

According to the value in a dword I've named <u>use_syscalls_flag</u>, the beacon will take one of three possible approaches.

- If the flag is equal to 1, it will call the desired syscall block directly; this means getting the correct code into eax is handled by the ntdll code.
- If the flag is equal to 2, it will call a function responsible for getting the appropriate code from syscall.syscall_code into eax and jumping to the SYSCALL instruction.
- If the flag is neither 1 or 2, it will simply call an API instead.

If a syscall is made by either method, the code will return 1 in eax. Otherwise, it returns the result from the standard API that was called. The presence of the flag leads me to think all beacons will have the mechanisms for handling syscalls. Choosing to use indirect syscalls in the builder would simply set the appropriate flag(s) in the binary, instead of producing a payload that doesn't handle syscalls at all.

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