Malware analysis - part 1: My intro to x86 assembly.

cocomelonc.github.io/tutorial/2021/10/03/malware-analysis-1.html

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15 minute read

Hello, cybersecurity enthusiasts and white hackers!



malware analysis

Any person who considers himself close to the world of information security, especially malware analysts (blue team) and exploit developers (red team), must have a basic understanding of assembly language.

As I wrote earlier in my posts, I came to cybersecurity with programming experience, but I only have experience in red team scenarios, so I want to try up my skills in blue team, especially in malware analysis.

Today, malware analysis is a whole industry in the field of information security. Antivirus engines laboratories that release their own protection products, highly specialized groups of experts striving to be in the trend of attack vectors, and even malware writers themselves, who compete for a potential client - "victim", are also involved in it.

So I will start a series of articles dedicated to my path in learning this craft.

I really hope that this will help at least one person other than me.

Let's go!

In traditional computer architecture, a computer system can be represented as several levels of abstraction that create a way of hiding the implementation details.

For simplicity, we will assume that we have three levels of coding when analyzing malware.



Machine CPU code

This is very simplest model, in real life computer systems are generally described with more than three levels of abstraction:

HARDWARE - the hardware level, the only physical level, consists of electrical circuits that implement complex combinations of logical operators such as XOR, AND, OR, and NOT gates, known as digital logic. Because of its physical nature, hardware cannot be easily manipulated by software.

MICROCODE - also known as firmware. Microcode operates only on the exact circuitry for which it was designed. It contains microinstructions that translate from the higher machinecode level to provide a way to interface with the hardware. **MACHINE CODE** - the machine code level consists of opcodes, hexadecimal digits that tell the processor what you want it to do. It's not just one language called machine code. It's many different kinds of machine code. Just as we speak different languages as people, machines speak different languages.

LOW-LEVEL LANGUAGES - a low-level languages is a human-readable version of a computer architecture's instruction set. The most common low-level language is assembly language. Assembly language which corresponds to the different architectures is by far the most important tool in any malware analyst's toolkit.

HIGH-LEVEL LANGUAGES - Most computer programmers include who write malware, operate at the level of high-level languages. High-level languages provide strong abstraction from the machine level and make it easy to use programming logic and flow-control mechanisms.

INTERPRETED LANGUAGES - Interpreted languages are at the top level. The code at this level is not compiled into machine code, instead, it is translated into bytecode. Bytecode executes within an interpreter, which is a program that translates bytecode into executable machine code on the fly at runtime. For example, python. Python is well suited for quick malware analysis. For example, a library such as <u>pefile</u>. In one of the following posts I will show an example of using this library.

The term *"reverse engineering"* has several popular meanings. In my case, I am considering researching compiled programs (malware). When we disassemble malware, we take the malware bin as input then we generate assembly language code as output, usually with a disassembler.

I think many more experienced malware analysts will agree with me if I start with a short introduction to assembly language x86.

the x86 architecture

At this time there two main architectures that indicate how our programs is compiled and executed: 32-bit and 64-bit. We will be going over the 32-bit architecture (x86) and 32-bit (x86) assembly language.

The internals of most modern computers architectures, including x86, follow the Von Neumann architecture:

CPU (Central Processing Unit) - executes code.

RAM - the main memory of the system stores all data and node.

I/O - an input/output system interfaces with devices such as hard drives, keyboards, printers etc.



As you can see CPU contains several components:

The *control unit* gets instructions to execute from RAM using a *register* - the *instruction pointer*, which stores the address of the instruction to execute.

Registers - Registers are small memory storage areas built into the processor (still volatile memory).

There are 8 "general purpose" registers:

- EAX stores function return values
- **EBX** base pointer to the data section
- ECX counter for string and loop operations
- EDX I/O pointer
- **ESI** source pointer for string operations
- EDI destination pointer for string operations
- ESP stack pointer
- **EBP** stack frame base pointer

And instruction pointer:

EIP - pointer to next instruction to execute - "instruction pointer"

The ALU - Arithmetic logic unit executes an instruction fetched from RAM and places the results in registers or memory. The process of fetching and executing instruction after instruction is repeated as a program runs.

The main memory **RAM** for a single program can be divided into the following 4 main sections:



The **Data** section contains values that are put in place when a program is initially loaded. **Code** includes the instructions fetched by the CPU to execute the program's tasks. **Heap** is used for dynamic memory during program execution, to allocate new values (for example malloc() and calloc() functions in C) and eliminate (for example free() function in C) values that the program no longer needs.

The **Stack** is used for local variables and parameters for functions and to help control program flow.

instructions

Instructions are building blocks of assembly programs. In x86 assembly, an instruction is made of a *mnemotic* and 0 or more *operands*:



The **EFLAGS** register holds many single bit flags.

For now, we remember the following flags:

ZF - Zero Flag - Set if the result of some instruction is zero; cleared otherwise

SF - Sign Flag - Set equal to the most-significant bit of the result, which is the sign bit of a signed integer: 0 - indicates positive value, 1 - indicates a negative value

NOP nop - first x86 instuction, no operation! no registers! no values! This instruction just for paddding bytes or delay time.

Red teamers use it to make simple exploits more reliable.

Before looking at other instructions, we need to elaborate on the concept of a stack in memory.

The Stack is conceptual are of main memory (RAM) which is designated by the operating system when program is started. A stack is a LIFO (Last-In-First-Out) data structure where data is "pushed" on to the top of the stack and "popped" off the top. By convention the stack grows toward lower memory addresses:



The Stack is logically divided into many **Stack Frames**.



The newest stack frame is indexed as *Stack Frame 0*, the older one *Stack Frame 1*, and the oldest Stack Frame is indexed *Stack Frame (count - 1)*

The current stack frame (*Stack Frame 0*) is always the newest Stack Frame.

A stack frame is represented by two pointers:

Base pointer saved in EBP register - the memory address that is equal to (EBP-1) is the first memory location of the stack frame.

Stack pointer saved in ESP register - the memory address that is equal to (ESP) is the top memory location of the stack frame.



When *Pushing* or *Popping* values, ESP register value is changed (the stack pointer moves) Base pointer value in EBP never change unless the current Stack Frame is changed. The Stack Frame is empty when EBP value = ESP value.

All the space between these two registers make up the **Stack Frame** of whatever function is currently being called.

So, whenever a function is called a new Stack Frame is created. Local variables are also allocated at the bottom of the created Stack Frame. To create a new Stack Frame, simply change EBP value to be equal to ESP:

0xFFFFFFFF



Now EBP = ESP, this means that the newest Stack Frame is empty. The previous stack frame now is indexed as Stack Frame 1.

But there are the caveat. **This time we should save EBP value before changing it!**. First, *PUSH* value of EBP to save it:

0xFFFFFFFF



Then change the value of EBP:

0xFFFFFFFF



All the stack addresses outside of the current stack frame are considered to be junked by the compiler.

PUSH - push word, dword, qword onto the Stack.

For our purposes, it will always be a DWORD (4 bytes). Can either be an immediate (a numberic constant), or the value in a register. The push instruction automatically decrements the stack pointer ESP by 4.

For example:

push eax, 0x00000003



POP - pop a value from the Stack.

Take a DWORD off the stack, put it in a register, and increment ESP by 4. For example:

```
pop eax
```



Before proceeding with other instructions let's focus on call types (or calling convensions).

Calling conventions are a standardized method for functions to be implemented and called by the machine. A calling convention specifies the method that a compiler sets up to access a subroutine. Calling conventions specify how arguments are passed to a function, how return values are passed back out of a function, how the function is called, and how the function manages the stack and its stack frame. In short, the calling convention specifies how a function call in C or C++ is converted into assembly language.

There are many call types, two of them are commonly used in most programming languages:

cdecl - the default call type for C functions. The caller is responsible of cleaning the stack frame.

stdcall - the default call type for Win32 APIs. The callee is responsible of cleaning the stack frame.

CALL - call procedure.

This instruction job is to transfer control to a different function, in way that control can later be resumed where it left off. First it pushes the address of the next instruction onto the stack. Then it changes **EIP** to the address given in the instruction. Destination address can be specified in multiple ways:

- Absolute address
- Relative address (relative to the end of the instuction)

RET - return from procedure.

There are two forms of this instruction:

Pop of the top of the stack into EIP, just written:

ret

Typically used by cdecl functions.

Pop of the top of the stack into EIP and add a constant number of bytes to ESP:

```
ret 0x8
;....
;....
ret 0x20
```

Typically used by stdcall functions.

MOV - can move:

- 1. register to register
- 2. memory to register, register to memory
- 3. immediate to register, immediate to memory
- 4. Never! memory to memory

Examples:

```
mov eax, ebx; copies the contents of EBX to the EAX registermov eax, 0x42; copies the value 0x42 into EAX register
```

first x86 assembly language program

I appreciate everyone for your patience, if you have read this far. So finally we can try to code our first program in assmebly language. As I said earlier, we are going to create 32-bit assembly programs as most malware is written in 32-bit mode, but keep in mind: most of us all have 64-bit operating systems nowadays, 32-bit programs can run on them.

Let's go! I will write our programs for linux, I choose Ubuntu 16.04 64-bit. First of all, please install NASM:

sudo apt-get install nasm

Then create test1.asm with following code:

```
; first program in asm
; author @cocomelonc
section .data
section .bss
section .text
  global _start ; must be declared for linker
                ; linker entry point
_start:
 mov eax, 100 ; mov 100 into the EAX register
; normal exit
exit:
 mov eax, 1
                 ; sys_exit system call
                ; exit code 0 successfull execution
 mov ebx, 0
 int 0x80
                ; call sys_exit
```

Every assembly language program is divided into three sections:

data section - this section is used for declaring initialized data or constants as this data does not ever change at runtime. You can declare constant values, buffer sizes, file names, etc.
bss section - this section is used for declaring uninitialized data or variables.
text section - this section is used for the actual code sections as it begins with a global __start which tells the kernel where execution begins.

Let's go to compile our program:

user@lu	ubuntu16: ~/code/test - + ×
File Edit View Search Terminal Help	
<pre>user@lubuntu16:~/code/test\$ nas user@lubuntu16:~/code/test\$ ld user@lubuntu16:~/code/test\$ ls total 12 -rwxrwxr-x 1 user user 520 Oct -rw-rw-r 1 user user 576 Oct -rw-rw-r 1 user user 378 Oct user@lubuntu16:~/code/test\$./t user@lubuntu16:~/code/test\$ obj test1: file format elf32-i3</pre>	sm -f elf32 -o testl.o testl.asm -m elf_i386 -o testl testl.o -lt 4 06:11 testl 4 06:10 testl.o 4 06:10 testl.asm testl jdump -d -M intel testl 386
Disassembly of section .text:	
08048060 <_start>: 8048060: b8 64 00 00 00	mov eax,0x64
08048065 <exit>: 8048065: b8 01 00 00 00 804806a: bb 00 00 00 00 804806f: cd 80 user@lubuntu16:~/code/test\$</exit>	mov eax,0x1 mov ebx,0x0 int 0x80

As you can see when we run it by ./test1 nothing happen. There is no output, and that's correct. Because, all we did was create a program which move 100 to EAX register and normally exit.

And as you can see from output of command:

objdump -d -M intel test1

user@lubunt	tu16:~/code/t	est\$	objdump	-d -M inte	l testl
test1:	file format	elf32	2-1386		
Disassembly	/ of section	.text	::		
08048060 <	start>:				
8048060:	b8 64 0	0 00	00	mov	eax,0x64
08048065 <	exit>:				
8048065:	b8 01 0	0 00	00	mov	eax,0x1
804806a:	bb 00 0	0 00	00	mov	ebx,0x0
804806f:	cd 80			int	0x80
user@lubun1	tu16:~/code/t	est\$			

Since we consider the study from the point of view of a malware analyst, objdump command is very important and must have knowledge for *static analysis*.

Static analysis is the process of analyzing malware "at rest", to extract identifying features and other characteristics from the tool without actually executing it.

The objdump utility is part of the binutils package, which is a bundle of tools used in Linux/UNIX systems for working with many core binary file types. The objdump utility is designed to be a full metadata analysis and reporting tool for executable files. Using the -d arguments, objdump can be told to disassemble the file.

Let's back to our code and examine 15-18 lines.

	exit:		
	mov eax, l		
	mov ebx, 0	; exit code 0 successfull execution	
	int 0x80		
19			

On line 16, in the Intel syntax we mov eax, 1 meaning we move the decimal value of 1 into eax which specifies the sys_exit call which will properly terminate program execution back to Linux so that there is no segmentation fault. (1)

Then on line 17 we mov ebx, 0 which moves 0 into ebx to show that the program successfully executed. (2)



All the syscalls are listed in /usr/include/asm/unistd_32.h, together with their numbers (the value to put in EAX before you call int 0x80).

And finally, on line 18 we see int 0×80 . Let's dive into this a little deeper.

In Linux, there are two distinct areas of memory. At the very bottom of memory in any program execution we have the Kernel Space which is made up of the Dispatcher section and the Vector Table.

At the very top of memory in any program execution we have the User Space which is made up of The Stack, The Heap and finally your code all of which can be illustrated in the below diagram:



When we load the values as we demonstrated above and call INT 0x80, the very next instruction's address in the User Space, ASM Code section which is your code, is placed into the Return Address area in The Stack. This is critical so that when INT 0x80 does its work, it can properly know what instruction is to be carried out next to ensure proper and sequential program execution.

Keep in mind in modern versions of Linux, we are utilizing Protected Mode which means you do NOT have access to the Linux Kernel Space. Everything under the long line that runs in the middle of the diagram above represents the Linux Kernel Space.

The natural question is why can't we access this? The answer is very simple, Linux will NOT allow your code to access operating system internals as that would be very dangerous as any Malware could manipulate those components of the OS to track all sorts of things such as user keystrokes, activities and the like.

In addition, modern Linux OS architecture changes the address of these key components constantly as new software is installed and removed in addition to system patches and upgrades. This is the cornerstone of Protected Mode operating systems.

Firstly, I will not necessarly look at malware as I would rather focus on the topics of assembly language programs that will give you the tools and undestanding, and I want to first learn to understand at least a little bit of any program in assembler, not only malware.

I will continue the basic statistical analysis of our file, in order to understand what other tools may be useful for malware analysis.

The file command is built in to pretty much every Linux and BSD. It is build around **libmagic** which is a library that can perform metadata analysis based upon arbitrary file structure information stored in a "magic database":

file test1



The strings tool is also part of the binutils package. This utility scans the file from beginning to end and attempts to discover strings that would be encoded using standard conventions, such as a sequence of human-readable characters followed by the 0 (NULL) byte (x00). The strings utility can be told to change its behavior to filter only to longer-sized strings, and also can identify a number of different string encodings, such as the UTF-16 that is popular on Windows.

To show only 3-byte or greater strings:

strings -n 3 test1



And my favourite one is hexdump. The hexdump command in Linux is used to filter and display the specified files, or standard input in a human readable specified format. My favorite invocation of hexdump is using the -C option. This gives a 16-byte-wide hexadecimal dump output, as well as a preview of the raw text (sanitizing unprinable characters) on the right. This gives you the ability to see the numeric representation, as well as view the raw data for human-readable content or other patterns that are helped by a denser viewport.

					u	ser	@lı	ıbuı	ntu]	. 6:	~/c	ode	/te	st			- + ×
File Edit V	View	Se	arch	Те	rmir	nal	Help)									
user@lubu	ntu:	L6:-	~/co	ode/	/tes	st\$	hex	kdum	p -(C te	est:	L					
000000000	7f	45	4c	46	01	01	01	00	00	00	00	00	00	00	00	00	.ELF
00000010	02	00	03	00	01	00	00	00	60	80	04	08	34	00	00	00	
00000020	40	01	00	00	00	00	00	00	34	00	20	00	01	00	28	00	[@4(.]
00000030	05	00	02	00	01	00	00	00	00	00	00	00	00	80	04	08	
00000040	00	80	04	08	71	00	00	00	71	00	00	00	05	00	00	00	jgqj
00000050	00	10	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
00000060	b8	64	00	00	00	b8	01	00	00	00	bb	00	00	00	00	cd	j.dj
00000070	80	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
00000080	00	00	00	00	00	00	00	00	60	80	04	08	00	00	00	00	· [`
00000090	03	00	01	00	01	00	00	00	00	00	00	00	00	00	00	00	
000000a0	04	00	f1	ff	0b	00	00	00	65	80	04	08	00	00	00	00	e
000000b0	00	00	01	00	15	00	00	00	60	80	04	08	00	00	00	00	· [` [
000000c0	10	00	01	00	10	00	00	00	71	90	04	08	00	00	00	00	jj
000000d0	10	00	01	00	1c	00	00	00	71	90	04	08	00	00	00	00	
000000e0	10	00	01	00	23	00	00	00	74	90	04	08	00	00	00	00	j#tj
000000f0	10	00	01	00	00	74	65	73	74	31	2e	61	73	6d	00	65	test1.asm.e
00000100	78	69	74	00	5f	5f	62	73	73	5f	73	74	61	72	74	00	xit. bss start.
00000110	5f	65	64	61	74	61	00	5f	65	6e	64	00	00	2e	73	79	edata. endsy
00000120	6d	74	61	62	00	2e	73	74	72	74	61	62	00	2e	73	68	mtabstrtabsh
00000130	73	74	72	74	61	62	00	2e	74	65	78	74	00	00	00	00	strtabtext
00000140	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
*																	
00000160	00	00	00	00	00	00	00	00	1b	00	00	00	01	00	00	00	· · · · · · · · · · · · · · · · · · ·
00000170	06	00	00	00	60	80	04	08	60	00	00	00	11	00	00	00	· · · · · · · · · · · · · · · · · · ·
00000180	00	00	00	00	00	00	00	00	10	00	00	00	00	00	00	00	
00000190	11	00	00	00	03	00	00	00	00	00	00	00	00	00	00	00	

The next thing I want to do is let's take my test1 to <u>GDB debugger</u> tool, and examine what exactly is going on at the assembly level.

Before we start working with gdb, we need to install the gdb <u>peda</u> extension.

Let's begin by loading my binary to gdb.

Run:

gdb -q test1

۰. user@lubuntu16: ~/code/test - + × File Edit View Search Terminal Help user@lubuntu16:~/code/test\$ user@lubuntu16:~/code/test\$ gdb -q test1 Reading symbols from test1...(no debugging symbols found)...done. b start Breakpoint 1 at 0x8048060 Starting program: /home/user/code/test/test1 EAX: 0x0 EBX: 0x0 ECX: 0x0 EDX: 0x0 ESI: 0x0 EDI: 0x0 EBP: 0x0 ESP: 0xffffd2d0 --> 0x1 (< start>: mov eax,0x64) EIP: EFLAGS: 0x202 (carry parity adjust zero sign trap INTERRUPT direction overflow) 0x804805a: add BYTE PTR [eax],al 0x804805c: add BYTE PTR [eax],al 0x804805e: add BYTE PTR [eax],al => 0x8048060 < start>: mov eax,0x64

Let's fisrt set a breakpoint on start by typing: b _start (1)

Then we can run the program by typing: r (2)

Thanks to our peda extension, we see our registers and code:

	testinishi — /concytest — Atom
	user@lubuntu16: ~/code/test - + ×
Fil	e Edit View Search Terminal Help
gdt	i-pedaş r
Sta	rting program: /home/user/code/test/test1
ſ	registers
-]	registers
EA)	(: 0x0
EB)	(: 0x0
EC)	
EU/	
EDI	: 0x0
EBF	2: 0×0
ESF	e: 0xffffd2d0> 0x1
EIF	<pre>: 0x8048060 (<_start>: mov eax,0x64)</pre>
EFL	AGS: 0x202 (carry parity adjust zero sign trap INTERRUPT direction overflow
)	codo
-1	coue
	0x804805a: add BYTE PTR [eax],al
	0x804805c: add BYTE PTR [eax],al
	0x804805e: add BYTE PTR [eax],al
=>	0x8048060 <_start>: mov eax,0x64
	000000000000000000000000000000000000
	$0 \times 8048066 $ <exit+10>: int 0 x 80</exit+10>
	0x8048071: add BYTE PTR [eax],al
[stackstack

Let's begin disassembly, we type: disas

	testi.asm — ~/code/test — Atom
<u>E</u> dit <u>V</u> iew <u>Selection</u> Find <u>Packages</u> <u>H</u> elp	user@lubuntu16: ~/code/test - + ×
test1.asm	File Edit View Search Terminal Help
; first program in asm ; author @cocomelonc	0x804805e: add BYTE PTR [eax],al => 0x8048060 <_start>: mov eax,0x64 0x8048065 <exit>: mov eax,0x1 0x8048065 coxit=5: mov eax,0x1</exit>
section .data	0x804806f <exit+10>: int 0x80 0x804806f1: add BYTE PTR [eax].al</exit+10>
section .bss	[stack
<pre>section .text global _start ; must be declared for linker</pre>	0000 0xffffd2d0> 0x1 0004 0xffffd2d4> 0xffffd463 ("/home/user/code/test/test1") 0008 0xffffd2d8> 0x0 0012 0xffffd2dc> 0x6fffd47e ("XDG VTNR=7")
_start: ; linker entry point mov eax, 100 ; mov 100 into the EAX register ; normal exit	0016 0xffffd2e0> 0xffffd499 ("XDG_SESSION ID=c2") 0020 0xffffd2e4> 0xffffd49b ("SSH_AGENT_PID=2121") 0024 0xffffd2e8> 0xffffd4e ("XDG_GREETER_DATA_DIR=/var/lib/lightdm-data/u ser") 0028 0xffffd2ec> 0xffffd4de ("SAL USE VCLPLUGIN=gtk")
<pre>ext: mov eax, 1 ; sys_exit system call mov ebx, 0 ; exit code 0 successfull execut int 0x80 ; call sys_exit</pre>	[
	Dump of assembler code for function _start: => 0x080480660 <+0>: mov eax,0x64 End of assembler dump. gdb.pedas

Then we use the command si which means step-into to advance to the next instruction.

-			
<u>F</u> ile	Edit View Selection F	F <u>i</u> nd <u>P</u> ackages <u>H</u> elp	user@lubuntu16: ~/code/test - + ×
	test1.asm		File Edit View Search Terminal Help
1 2 3) [
4 5	section .data		0x804805d: add BYTE PTR [eax],al 0x804805f: add BYTE PTR [eax+0x64],bh
6 7	section .bss		=> 0x8048065 <exit>: mov eax,0x1 0x8048066 <exit>: mov ebx,0x0 0x8048066 <exit>: int 0x80</exit></exit></exit>
8	section .text		0x8048071: add BYTE PTR [eax],al
10	global_start ;		0x8048073: add BYTE PTR [eax],al
11	_start: ;		-1
12	mov eax, 100 ;		00000 0xtfffd2d0> 0x1 00041 0xffffd2d4> 0xffffd463 ("/home/user/code/test/test1")
13			0008 0xffffd2d8> 0x0
15	exit:		0012 0xffffd2dc> 0xffffd47e ("XDG VTNR=7")
16			0020 0xffffd2e4> 0xffffd49b ("SSH AGENT PID=2121")
17	mov ebx, 0 ;		<pre>c 0024 0xffffd2e8> 0xffffd4ae ("XDG_GREETER_DATA_DIR=/var/lib/lightdm-data/u</pre>
18	int 0x80 ;		Ser") 00281 0xffffd2oc> 0xffffd/do ("SNL USE VCLDLUCTN-a+k")
19			
			-] Legend: rode, data, rodata, value 0x08048065 in exit () odb-pedas

Again, thanks to peda, we see that simply moving 1 into EAX in exit() If you have not install peda extension, just type: disas:



As you can see, it's the same.

Then, repeat si command (or si and disas if you have not install peda):

<u>File E</u> dit <u>V</u> iew <u>S</u> election F <u>i</u> nd <u>P</u> ackages <u>H</u> elp	user@lubuntu16: ~/code/test - + ×
test1.asm	File Edit View Search Terminal Help
	<pre>=> 0x804806a <exit+5>: mov ebx,0x0 0x804806f <exit+10>: int 0x80 0x8048071: add BYTE PTR [eax],al 0x8048073: add BYTE PTR [eax],al</exit+10></exit+5></pre>
4 section .data 5	0x8048075: add BYTE PTR [eax],al
6 section .bss	-] 0000 0xffffd2d0> 0x1
8 section .text 9 global _start ; must be declared for linker	0004 0xffffd2d4> 0xffffd463 ("/home/user/code/test/test1") 0008 0xffffd2d8> 0x0 0012 0xffffd2dc> 0xffffd47e ("XDG VTNR=7")
10 11 _start: ; linker entry point	0016 0xffffd2e0> 0xffffd489 ("XDG_SESSION_ID=c2") 00200 0xffffd2e4> 0xffffd490 ("SSH AGENT PID=2121") 00201 0xffffd2e4> 0xffffd4e6 ("SSH AGENT PID=2121")
12 mov eax, 100 ; mov 100 into the EAX register 13	9024 0xffffd2ec> 0xffffd4e ("SAL USE VCLPLUGIN=gtk")
14 ; normal exit 15 exit:	
	Legend: code, data, rodata, value
1/ mov edx, 0 ; exit code 0 successfull execut.	adh-nedas disas
19	Dump of assembler code for function exit:
	0x08048065 <+0>: mov eax,0x1 => 0x0804806a <+5>: mov ebx,0x0 0x0804806f <+10>: int 0x80 End of assembler dump. ad0-pedas

and repeat again si:



and next step:

۶. user@lubuntu16: ~/code/test + xFile Edit View Search Terminal Help BYTE PTR [eax],al 0x8048075: add 0x8048077: add BYTE PTR [eax],al 0000| 0xffffd2d0 --> 0x1 0004 0xffffd2d4 --> 0xffffd463 ("/home/user/code/test/test1") 0008 0xffffd2d8 --> 0x0 0xffffd2dc --> 0xffffd47e ("XDG VTNR=7") 0012 0xffffd2e0 --> 0xffffd489 ("XDG SESSION ID=c2") 00161 0xffffd2e4 --> 0xffffd49b ("SSH AGENT PID=2121") 0020 00241 0xffffd2e8 --> 0xffffd4ae ("XDG GREETER DATA DIR=/var/lib/lightdm-data/u ser") 0028| 0xffffd2ec --> 0xffffd4de ("SAL USE VCLPLUGIN=gtk") ode, data, rodata, value Legend: 0x0804806f in exit () disas Dump of assembler code for function exit: 0x08048065 <+0>: eax,0x1 mov ebx,0x0 0x0804806a <+5>: mov => 0x0804806f <+10>: 0x80 int End of assembler dump. si [Inferior 1 (process 12264) exited normally] Warning: not running

So, as you can see our program exited normally as expected.

With each subsequent post in this series, I will analyze more and more complex examples and try to reverse the more interesting variants of malware. But, of course, I'll start with simple examples.

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Source code in Github

Thanks for your time and good bye! *PS. All drawings and screenshots are mine*